



||Jai Sri Gurudev ||

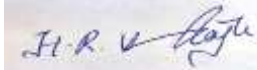
S J C Institute of Technology, Chickballapur – 562 101

RESUME

Name	Dr.VARADARAJU H R			
Date of Birth	29.05.1976			
Address	Contact Address: <i>Govardhana Nilaya,</i> Opp.to Karnataka State ware House Vasavi Nagar, Sira Road, Siragate, Tumkur 572106, Karnataka Residential Address: <i>C/o Nagaraju,</i> <i>Amma Nilaya</i> <i>Behind Srirama Temple, Agalagurki</i> Chickballapur – 562101, Karnataka Contact Nos: +91-9448769592 E-mail: varadarju.aiml@sjcit.ac.in Mobile: +91-448769592			
Department / Discipline				
Educational Qualifications	Exam Passed	Institution University	% & Class Obtained	Year
	Degree: B.E	SIT, Tumkur, Bangalore University	61.94 First Class	1998
	PG: M.Tech	SSIT, Tumkur, V T U, Belagavi	82.67 Distinction	2010
	Higher: PhD	Dr.M G R Educational and Research University, Chennai	Awarded	2020
Experience	Nature of Experience		No. of Years	
	Teaching		24	
	Industry/Research		01	
	Total No. of years of Experience		25	
Experience Details	Designation	Institution/ Organization	Duration	
	Associate Professor	SJCIT, Chickballapur	12/10/2023 To Till date	
	Professor & Head	AIT, Tumkur	9 Years 10 Months	
	Associate Professor	RRIT, Chickbaanavara, Bangalore	2 years 3 Months	
	Assistant Professor	S I E T, Tumkur	3 Years	
	Senior Lecturer	S I E T, Tumkur	4 Years 4 Months	
	Lecturer	SJMIT, Chitradurga	7 Months 12 Days	
	Lecturer	SIET Tumkur	6 years 5months 22Days	
Professional bodies Membership details	Life Member of ISTE, LM48102			
Other Professional Experience				

Areas of Research Interest & Guidance	Embedded Systems, VLSI, Biomedical, Communication
Distinctions/Awards Received	08
National/ International Work Shops/ Seminars / Conferences Attended	24
No. of Papers Presented/ Books Published	9 (Papers Published and Presented)
	1. “Production Enhancement using BIST” in National level seminar LIVAZ-10 conducted in Shirdi Sai College , Bangalore. 2. Low Cost BIST” in National Level Conference NCIIT-10 held in RRIT, Bangalore 3. Varadaraju, HR, Vasudevan, N & Ravi, S 2015 „Implementation of a Reconfigurable Decimator Using FPGA”, International Journal of Applied Engineering Research, vol. 10, no. 1, pp. 857-863 (Scopus Indexed) 4. Varadaraju, HR & Senthil Kumar, K 2017 “Performance Analysis to identify the type of digital modulation using Plug and Play Module”, Journal of Advanced Research in Dynamic and Control System, vol. 9, no. 12, pp. 1879-1889 (Scopus Indexed) 5. Mr. Varadaraju H R and Mrs. Deepa Deganvi Veeresh, 2020 “Solar Powered Smart Modular Helmet and Tail PIPE Exhaust Gas Monitoring and Alert System for Bike”, International Journal of Advances in Engineering and Management (IJAEM), Vol 2, Issue 1, pp: 1263-1267. 6. Dr.P.Supraja, Dr.Varadaraju H R et.al, 2022 “Optimal routing Protocol using a Multiverse optimizer Algorithm for wireless mesh Network”, International Journal of Communication Networks and Information Security, vol 14, Issue 03, pp. 36-46. 7. Dr.A.Helen Victoria, Dr.Varadaraju H R et.al, 2022 “Radar Based activity recognition using CNN-LSTM Network Architecture”, International Journal of Communication Networks and Information Security, vol 14, Issue03, pp. 303-312. 8. Mrs.Radha, Mrs.Padmavathi.N and Dr.Varadaraju H R., 2023 “Detection of brain Tumor from MRI Scans using Advanced Image processing Techniques”, International Journal of Advance Research and Innovative ideas in Education, E-Journal Volume-9 Issue-4, 2023, ISSN: 2395-4396.Paper Id.21375. 9. Aishwarya P R, Dr.Varadaraju H R, Bharathi N., “Implementation of Plant Leaf disease detection using K-means Clustering and neural networks” International Journal of Advance Research and Innovative ideas in Education, E-Journal Volume-9 Issue-4, 2023, ISSN:2395-4396. Paper Id.21377.

Date: 06.09.2024


 Signature of the Candidate